

FIG.1

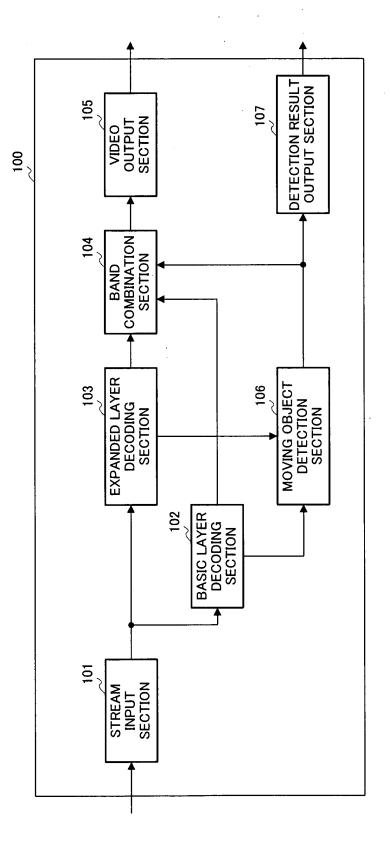


FIG.2

	BIT PLANE 1	BIT PLANE 2											•	
	0_	_0_	0	0	Ô	1	1	0	-	1	0	0		
		0	0	0	0	0	1	1	0	0	0	-		
	0	_0_	0	0	-	-	0	0	1	1	_	-		
	0	_0_	0	0 .	0	0	1	0	0	1.	0	0		
	0_	1	-	0	0	1	0	-	1	0	1	0		
	0	0_	0	0	1	0	1	0	1	1	0	_		FIG.3
	0	_0_	0	-	1	0	1	-	0	1	0	-		豆
	0_	_0_	0	0	0	-	0	0	-	0	-	0		
	0		0	-	0	0	1	0	1	1	-	0	:L 2	
	- - 1 -	1-1-1-		0	0	0		0	0	0	-0		PIXEL 2	
(10		- <u>-</u> -	 -	0	0	0	0		1	- <u>-</u> -	0	PIXEL 1	
BIT POSITION	▼												PIXE	

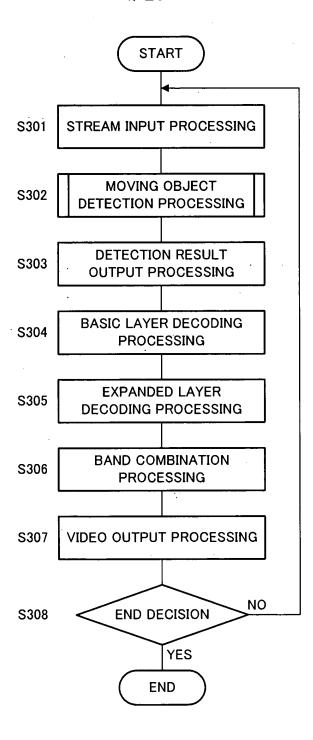


FIG.4

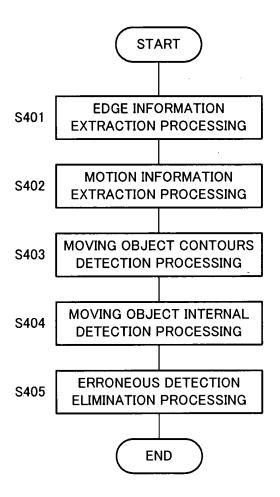


FIG.5

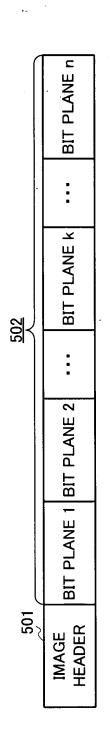


FIG.6

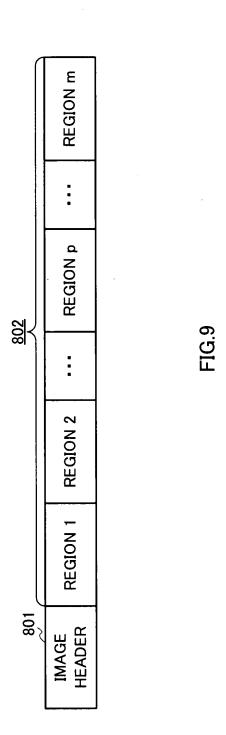
	BIT PLANE k OF REGION m
	•
<u>602</u>	BIT PLANE k OF REGION J
99	•
	1 2 X
	BIT PLANE OF REGION
	BIT PLANE BIT PLANE K BIT PLANE K HEADER OF REGION 1 OF REGION 2

FIG.7

701 702

PIXEL COMPONENT CODE SIGNAL OF REGION

FIG.8



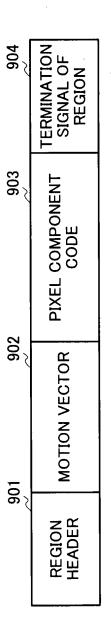
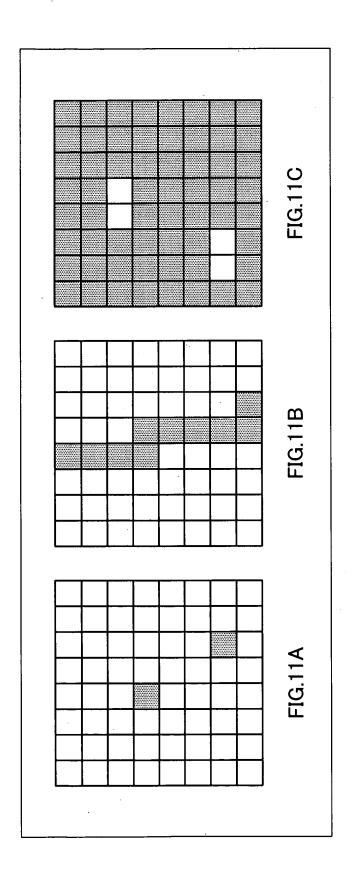


FIG. 10



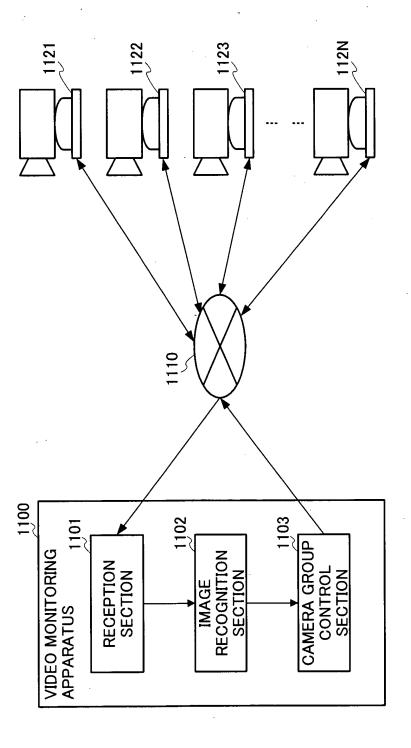


FIG.12

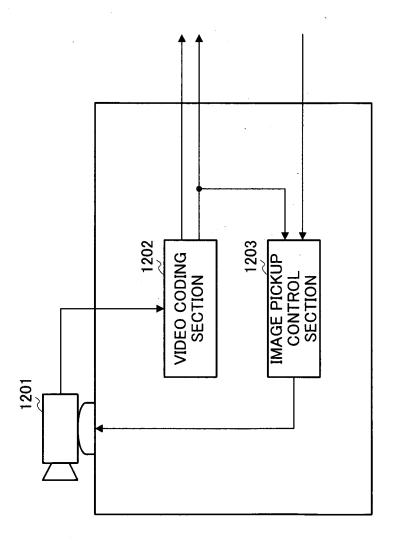


FIG.13

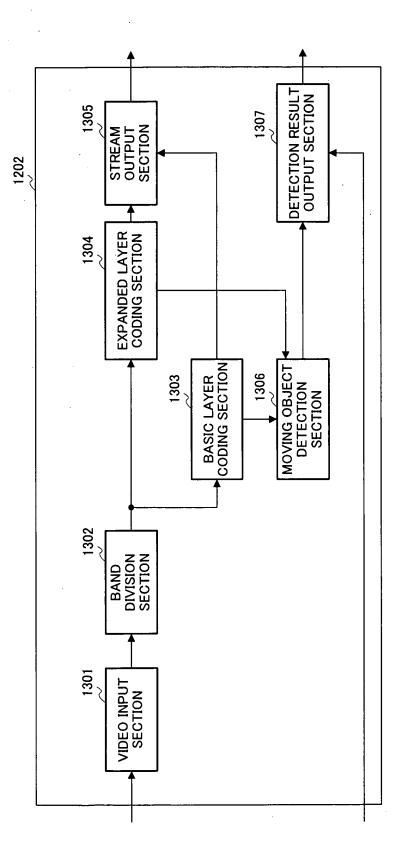


FIG.14

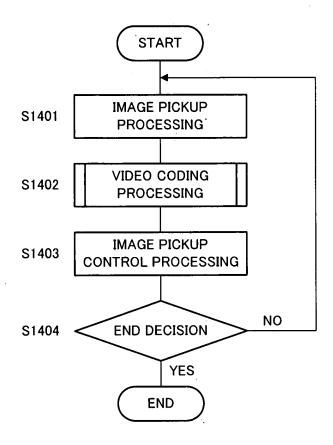


FIG.15

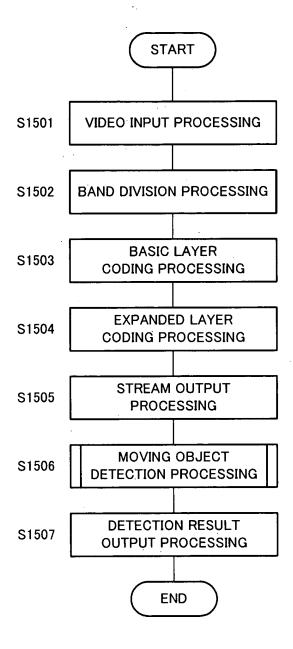


FIG.16

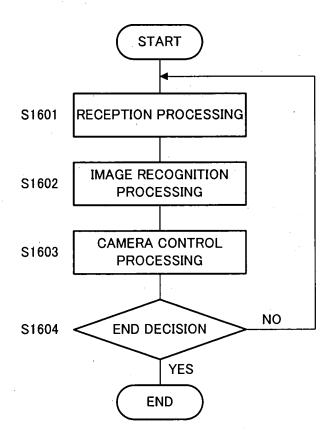


FIG.17

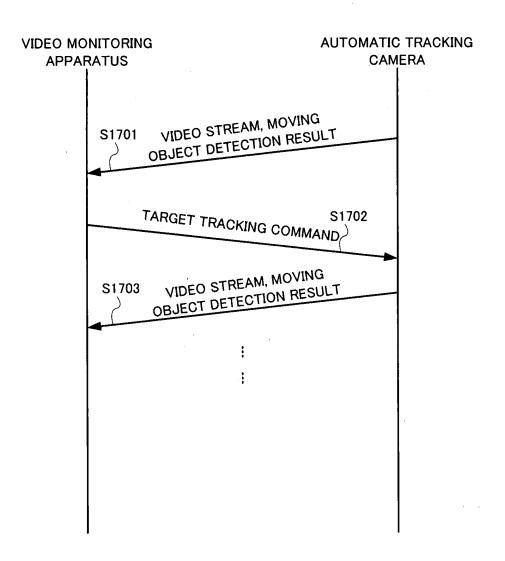


FIG.18

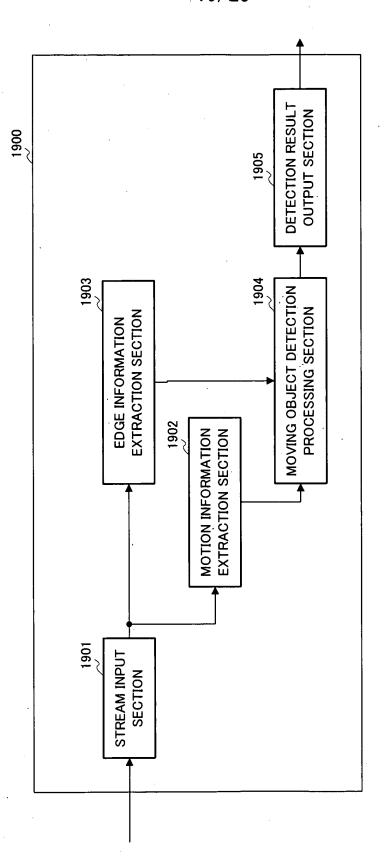


FIG.19

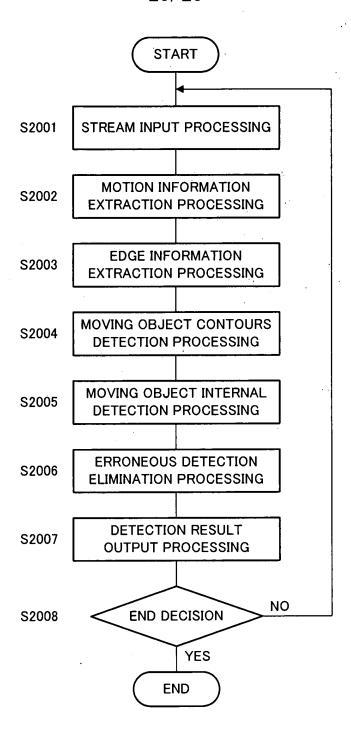


FIG.20